

THE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231

Sir:

Transmitted herewith for filing is the Utility patent application of:

INVENTOR: YOSHIO TAKAZAWA, TOSHIO YAMADA, SHINICHI OZAWA,
TAKEO KANAI, MINORU KATOH, KOUDOU YAMAUCHI AND
TOSHIHIRO ARAKI

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT AND IC CARD

17497 U.S. PTO
10/748137



Enclosed are:

- ☒ 21 sheets of ☐ formal ☐ informal drawings.
- ☒ Transmittal of Japanese applications 2003-016573 and 2003-370078 to claim priority dates of January 24, 2003 and October 30, 2003, respectively.
- ☒ An Executed Declaration.
- ☒ A Preliminary Amendment
- ☒ An assignment executed by inventors to Renesas Technology Corp and Hitachi ULSI Systems Co., Ltd..
- ☒ The filing fee is calculated as shown below:

FOR	NUMBER FILED		NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	17	-20	0	x\$18 =	0.00
INDEPENDENT CLAIMS	7	-3	4	x\$86 =	344.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable)				+\$290 =	
				BASIC FEE	\$770.00
				TOTAL OF ABOVE	1,114.00
REDUCTION BY 1/2 FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1.27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED					
				TOTAL	1,114.00

- ☒ A check in the amount of **\$1,154.00** to cover the filing fee and assignment fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to **Deposit Account Number 08-1480**.

Respectfully submitted,

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